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Integrated Information in Discrete Dynamical Systems: Motivation and Theoretical Framework



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It is possible, but inefficient, to build systems with high  $\varphi$  using grid architectures. As a point of comparison, the 8-element AND-gate network in Figure 12 generates  $\varphi = 3.75$ bits, considerably more than the maximum attained (2.3 bits) by a  $3\times3$  grid of AND-gates. The inefficiency increases with the size of the grid; for example an *XOR*-grid of a million elements is needed to generate 1000 bits of integrated information.  $\varphi$  of a grid is limited by the interactions occurring along the perimeters of the parts, so that the expected value of  $\varphi$  for an *n*×*n* grid is proportional to *n* (see Text S1, section 6). More generally, threedimensional lattice interactions occur along the surfaces of the parts, so  $\varphi$  will be proportional to their surface area, a phenomenon similar to the holographic principle